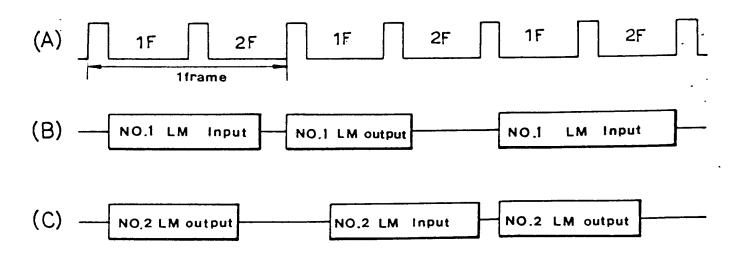


FIG.3



1.

88 **S3** Ŕ 8 (4 (RG.B.Syn) 9 8 DAC means Data Converting means Encoding means MPU Ri,GI,Bi Syni ADC means Video out 8 memory LINE 22 88 SYN cometing circuit Decoding intermediate gradation R2 Y G2 Y B2 SYN2 means **%** 2 S S TPH Separating means ς ζ 8 <svs

FIG. 4

**83** කි. ද **83** <u>S</u> 82a ജ് ଷ୍ଟ NO. 1 DPM NO. 2DPM NO.3 DPM F/Q/MADC means 56æ ä ভ -583 8 FROM MPU 58 & න් ක් address generating means generating means generating means address printing address monitoring 92 78 recording

 $6ic \longrightarrow 61$  (61a+61b+61c = 61) 70 DAC means 86 62

FIG. 5

.

